

Figure 1
(Prior art)

The diagram illustrates a cross-sectional view of a semiconductor device. At the top, a gate structure (102) is shown, connected to a power supply (110). Below the gate is a channel region (106) within a substrate (108). The substrate is supported by a base (112). Various other components are labeled, including 116, 118, 124, 126, 128, 130, 132, 149, and 150. A power supply (110) is connected to the gate (102) and the substrate (108). The diagram shows a complex arrangement of layers and structures, typical of a CCD or similar semiconductor device.

Figure 2
(Poster art)

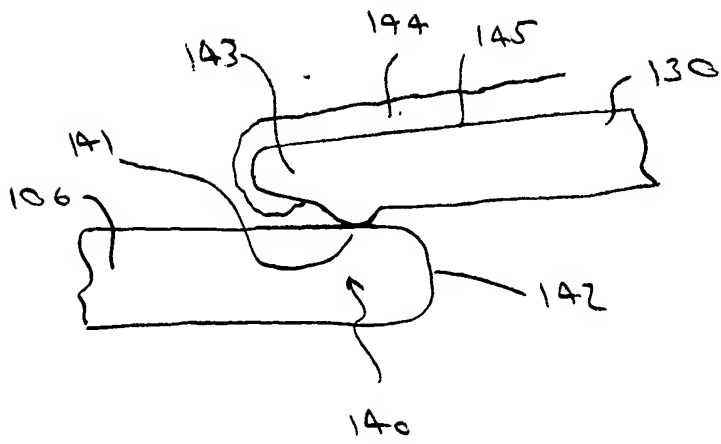


Figure 3

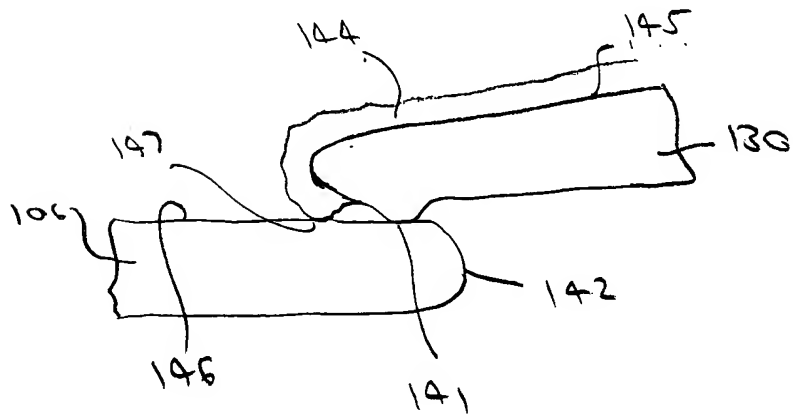


Figure 4

100

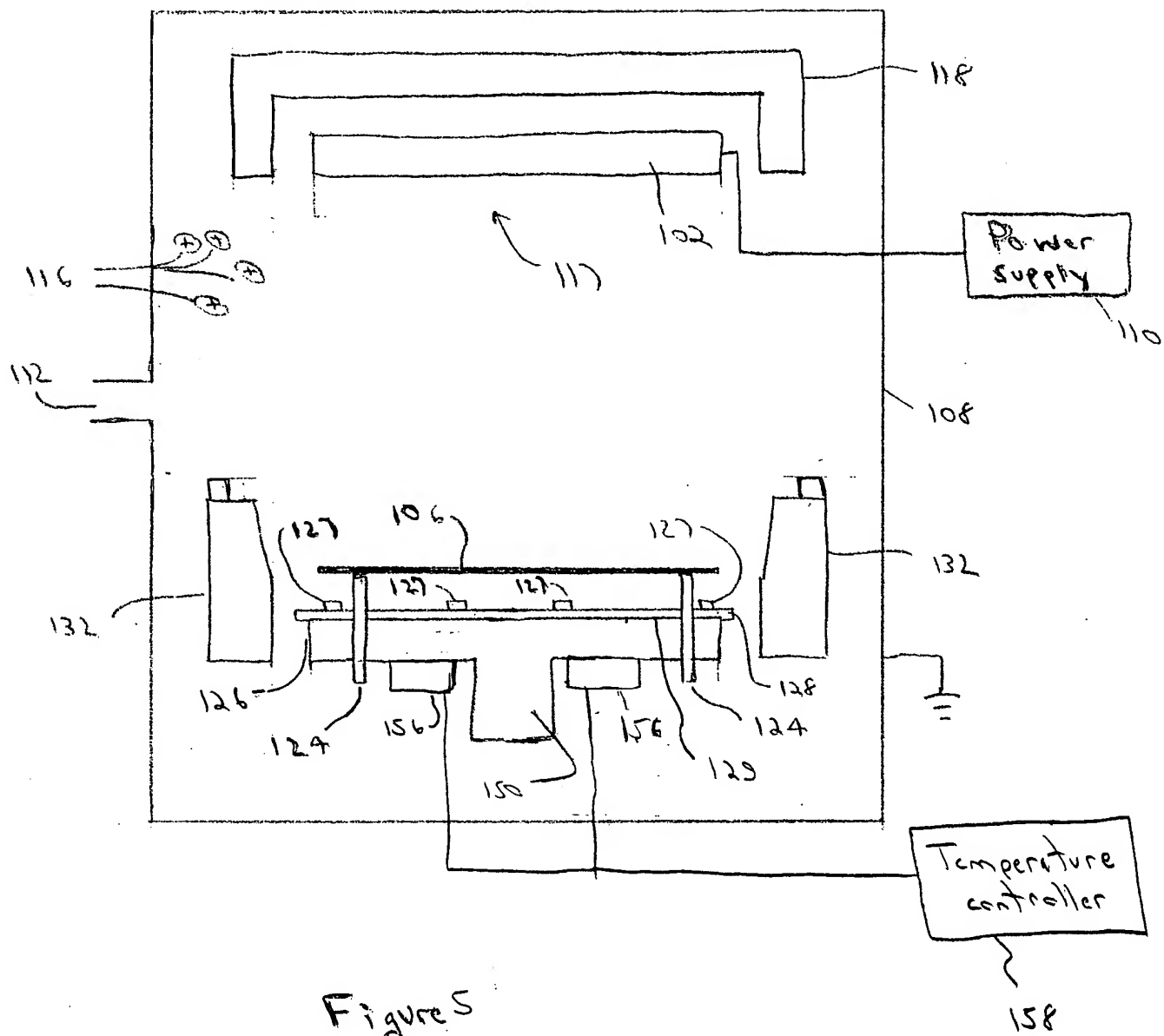


Figure 5

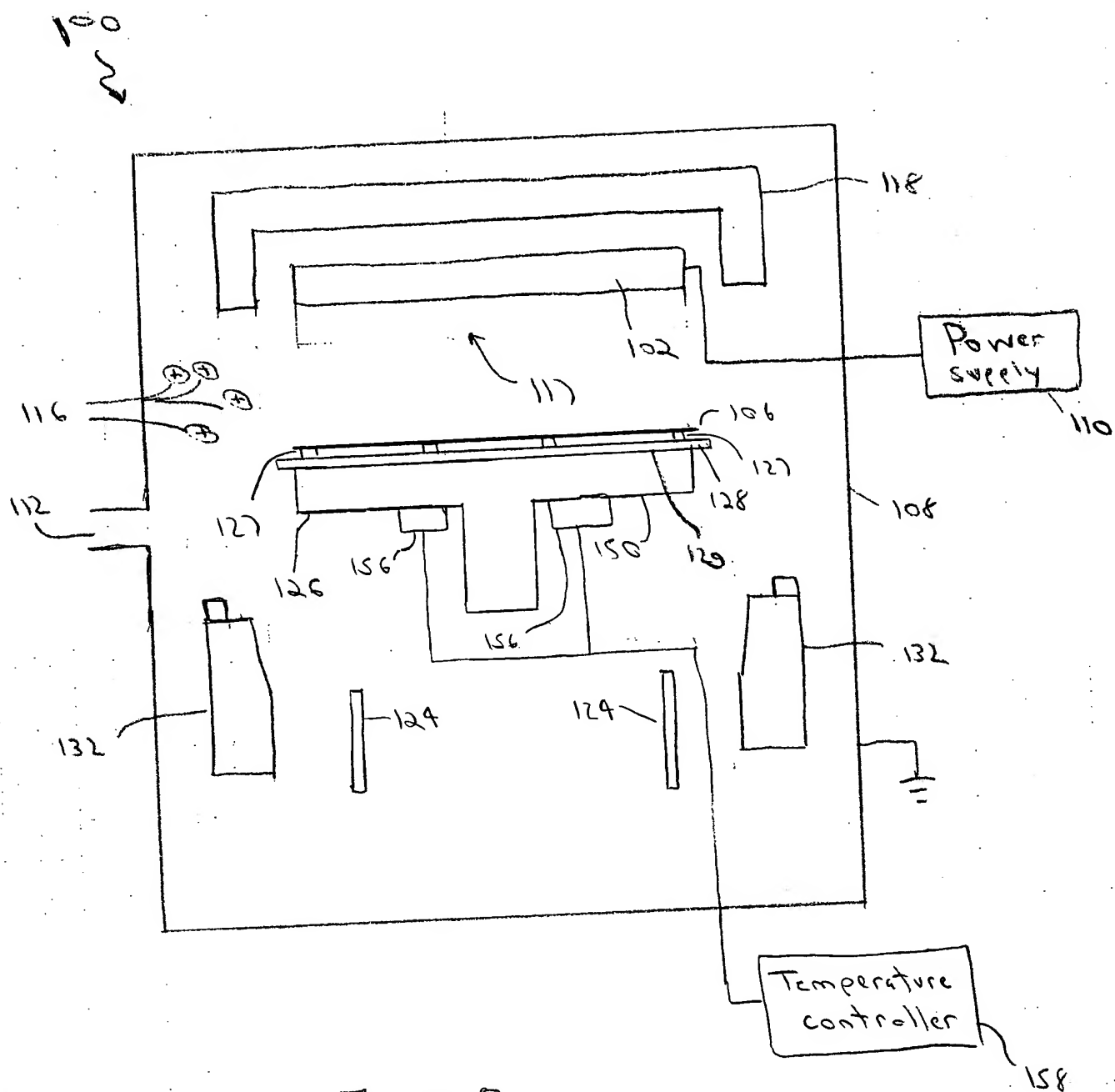


Figure 6